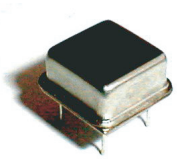

HCMOS/TTL WIDE TEMPERATURE CLOCK OSCILLATORS IN HALF SIZE DIP - XO08W Series

FEATURES

- Tight Frequency Stability over Extended Operating Temperature Range / COTS
- Very Low Phase Jitter with Fundamental or 3rd Overtone Crystal Design
- Tri-state Output Available, Hermetically Sealed, Industry Standard Lead Spacing
- Thru-hole PCB Applications in Environments Exposed to Temperature Extremes (-55°C to 105°C)

SPECIFICATIONS

Frequency Range	1 MHz to 60 MHz
Input Voltage (Vcc)	A = +5 VDC ± 10%; B = +3.3 VDC ± 10%
Input Current	40 mA Maximum, depending on frequency and output load
Storage Temperature	-55°C to 125°C
Overall Frequency Stability	100I = ±100 ppm/-55°C to 105°C; 50I = ±50 ppm/-55°C to 105°C
OperatingTemperature Range	I = -55°C to 105°C
Electric Option (Symmetry)	0 = No tristate 60/40%; 2 = No tristate 55/45% 1 = Tristate 60/40%; 3 = Tristate 55/45%
Output Load	HCMOS: Drive up to 50 pF load; TTL: Drive up to 10 TTL gates
Logic "1" / Logic "0" Level	0.9Vcc Minimum / 0.1Vcc Maximum
Rise/Fall Time (Tr/Tf)	10 ns Maximum
Start-up time	10 ms Maximum
Phase Jitter	1 ps Maximum at 1Sigma for fj > 1 kHz
Aging	3 ppm First year; 1 ppm/year after first year
Tristate Function	Input (Pin 1) High (> 2.2V) or open: Output (Pin 8) active Input (Pin 1) Low (< 0.8V): Output disabled in high impedance
Enable Time	100 ns Maximum
Typical Part Number	XO08W-Frequency-Vcc-Freq. Stability-Temperature Range-Tristate/Duty cycle
P/N Example	XO08W-50M000-A50I2: HCMOS/TTL clock in 8-pin DIP metal package, 50 MHz, +5 VDC, ±50 ppm / -55°C to 105°C, Non tristate, Duty cycle: 55/45
Notes	Serialized temperature test data available at additional cost

OUTLINE DRAWING